



**TIME TABLE**  
**UNIVERSITY COLLEGE OF ENGINEERING & TECHNOLOGY FOR WOMEN**  
**KAKATIYA UNIVERSITY CAMPUS, VIDYARANYAPURI, WARANGAL – 506 009**  
**B. TECH II/IV YEAR EEE (II-Sem)**

CLASS IN CHARGE: S.SAHITHI

ROOM NO: 38  
W.E.F: 02-01-2023

TIME/ DAY	9.30 to 10.20	10.20 to 11.10	11.10 to 11.20	11.20 to 12.10	12.10 to 1.00	1.00 to 2.00	02.00 to 2.50	2.50 to 3.40	3.40 to 4.30
MON	SEMINARS		<b>B R E A K</b>	EM-I LAB/EC LAB		<b>L U N C H</b>	PS-II	PS-II	SPORTS
TUE	SEMINARS			EM-I LAB/EC LAB			DELD LAB		SPORTS
WED	PE	PE		PS-II	PS-II		EC-II	EC-II	SPORTS
THU	LIBRARY	PE		DELD	DELD		DELD LAB		SPORTS
FRI	DELD	PE		EM-II	EM-II		EC-II	EC-II	SPORTS
SAT	EM-II	EM-II		COI	COI		SEMINARS	SEMINARS	SEMINARS

Electric Circuits-II (EC-II): S.Goverdhan  
Digital Logic Design (DELD): N. Srividya  
Electrical Machines -II (EM-II): T. Venkatesh  
Power Systems -II (PS-II): S.Sshithi  
Constitution of India (COI): Dr.K.Prasanna  
Power Electronics (PE): M. Surendar  
Digital Logic Design (DELD) LAB: 1. K.Sai Tarun 2. D. Ashok Kumar  
Electric Circuits (EC) LAB: 1. S.Sshithi 2. M. Surendar  
Electrical Machines -I (EM-I) LAB: 1. T. Venkatesh 2. Y.Yashashwini

  
In-charge Time Table

  
I/c HOD

  
Academic Co-Ordinator

  
PRINCIPAL



**TIME TABLE**

**UNIVERSITY COLLEGE OF ENGINEERING & TECHNOLOGY FOR WOMEN  
KAKATIYA UNIVERSITY CAMPUS, VIDYARANYAPURI, WARANGAL – 506 009  
B.TECH III/IV YEAR EEE (II-Sem)**

**CLASS IN-CHARGE:T.VENKATESH**

**ROOM NO: 40**

**W.E.F.: 02-01-2023**

TIME/ DAY	9.30 to 10.20	10.20 to 11.10	11.10 to 11.20	11.20 to 12.10	12.10 to 1.00	1.00 to 2.00	02.00 to 2.50	2.50 to 3.40	3.40 to 4.30
<b>MON</b>	PSD	PSD	<b>B R E A K</b>	MPS	LIBRARY	<b>L U N C H</b>	SEMINARS		
<b>TUE</b>	MPS	MPS		SEMINARS	SEMINARS		LIBRARY	PSOC	PSD
<b>WED</b>	EDS	PSOC		SS	SS		SEMINARS		
<b>THU</b>	PSOC	SS		MPS/CS LAB			SEMINARS		
<b>FRI</b>	PSOC	EDS		EDS	PSD		MPS/CS LAB		Sports
<b>SAT</b>	SEMINARS	SEMINARS		LIBRARY	LIBRARY		SGP	SGP	SGP

**Signals & Linear Systems (SLS):**

**K. Saitharun**

**Power Semiconductor & Drives (PSCD):**

**M. Surendar**

**Power Systems Operation & Control (PSOC):** T.venkatesh

**Switch Gear & Protection (SGP):**

**S.Govardhan**

**Microprocessors Systems (MPS):**

**G R Ramajyothi**

**Electrical Distribution Systems (EDS):**

**S.Sahithi**

**Microprocessors Systems (MPS) LAB:**

**1. Ashok 2. K. Saitharun**

**Control Systems (CS) LAB:**

**1. Y. Yashashwini 2. T.Venkatesh 3.S.Sahithi**

**In charge Time Table**

**I/c HOD**

**Academic Co-Ordinator**

**PRINCIPAL**



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UNIVERSITY COLLEGE OF ENGINEERING & TECHNOLOGY FOR WOMEN  
KAKATIYA UNIVERSITY CAMPUS, VIDYARANYAPURI, WARANGAL – 506 009  
B.TECH IV/IV YEAR EEE (II-Sem)

CLASS IN-CHARGE: Y YASHASWINI

ROOM NO: 41  
W.E.F: 02-01-2023

TIME/ DAY	9.30 to 10.20	10.20 to 11.10	11. 10 to 11. 20	11.20 to 12.10	12.10 to 1.00	1.00 to 2.00	02.00 to 2.50	2.50 to 3.40	3.40 to 4.30
WED	SEMINARS	SEMINARS	<b>B R E A K</b>	PROJECT WORK (M.S)	PROJECT WORK (M.S)	<b>L U N C H</b>	SEMINARS	SEMINARS	PROJECT WORK (Y.Y)
THU	SGT	SGT		OT	OT		ELECTRICAL SIMULATION LAB	PROJECT WORK (T.V)	
FRI	PROJECT WORK (S.S)	NCES		NCES	OT		ELECTRICAL SIMULATION LAB	PROJECT WORK (T.V)	
SAT	NCES	SGT		SGT	SEMINARS		PROJECT WORK (MS)	PROJECT WORK (T.V)	

Non-Conventional Energy Sources (NCES): Y.Yashashwini

Optimization Techniques(OT): A.Srinivas

Smart Grid Technology (SGT): S.Sahithi

Electrical Simulation Lab: 1. M.Surendar 2. T. Venkatesh

3. S.Sahithi

Project work: M.Surendar / T. Venkatesh

Y.Yashashwini / S.Sahithi

Seminars: M.Surendar / T. Venkatesh

Y.Yashashwini / S.Sahithi

Project Co-ordinator: S.Sahithi

  
In charge Time Table

  
I/e HOD

  
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